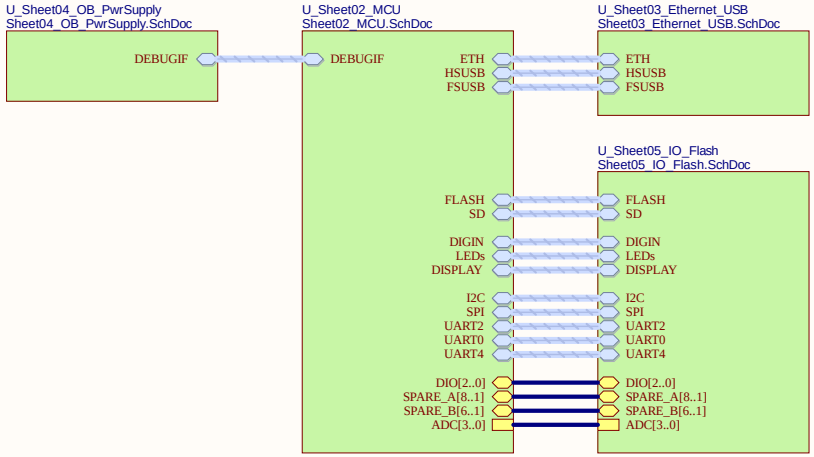
History / Changes

V2.0 Rev.A First public version

V2.0 Rev.B - Replaced PRTR5V0U4Y by NUP4114UCW1T2G

V2.0 Rev.C
- Used STM32F072CBU6 as OB chip (128k Flash instead of 64k)
- Connected J400.11/13 to GND to improve shielding on trace cable for TRACEDATA0 and RESET
- Specified Samtec part number for J400

V2.0 Rev.D
- Replaced F032 OB by K22 type
- Replaced Mini USB by Micro USB connector
- Added 2 GND hooks and testpoints for the LEDs
- Connected VREG_IN0 and VREG_IN1
- Connected J400.11/13 via capacitor to GND to allow for optional power supply here
- Replaced oscillator type EAA501 (obsolete)
- Replaced diodes BAR43C (obsolete)



Unless otherwise specified:
#NAME?
- Ceramic capacitors are of type X5R/X7R or C0G/NP0 with a voltage rating of at least 6.3V
- Resistors are 1% tolerance types

M100 M101 M102 M103 M108

M M M M

FID100 FID101 FID102

L W C U

History / Changes

(see left)

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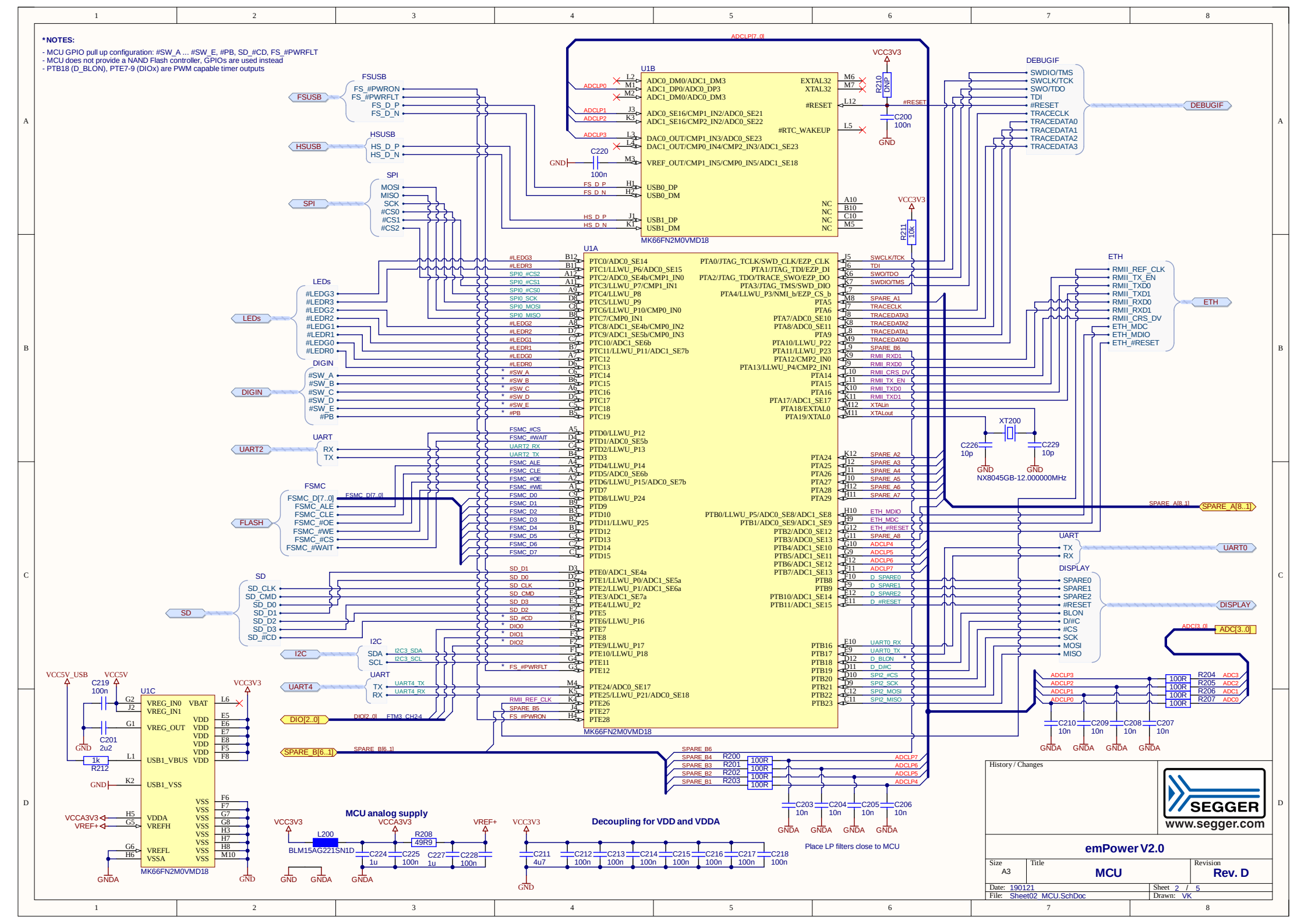
Size	Title	Revision
A3	**Top Level**	**Rev. D**

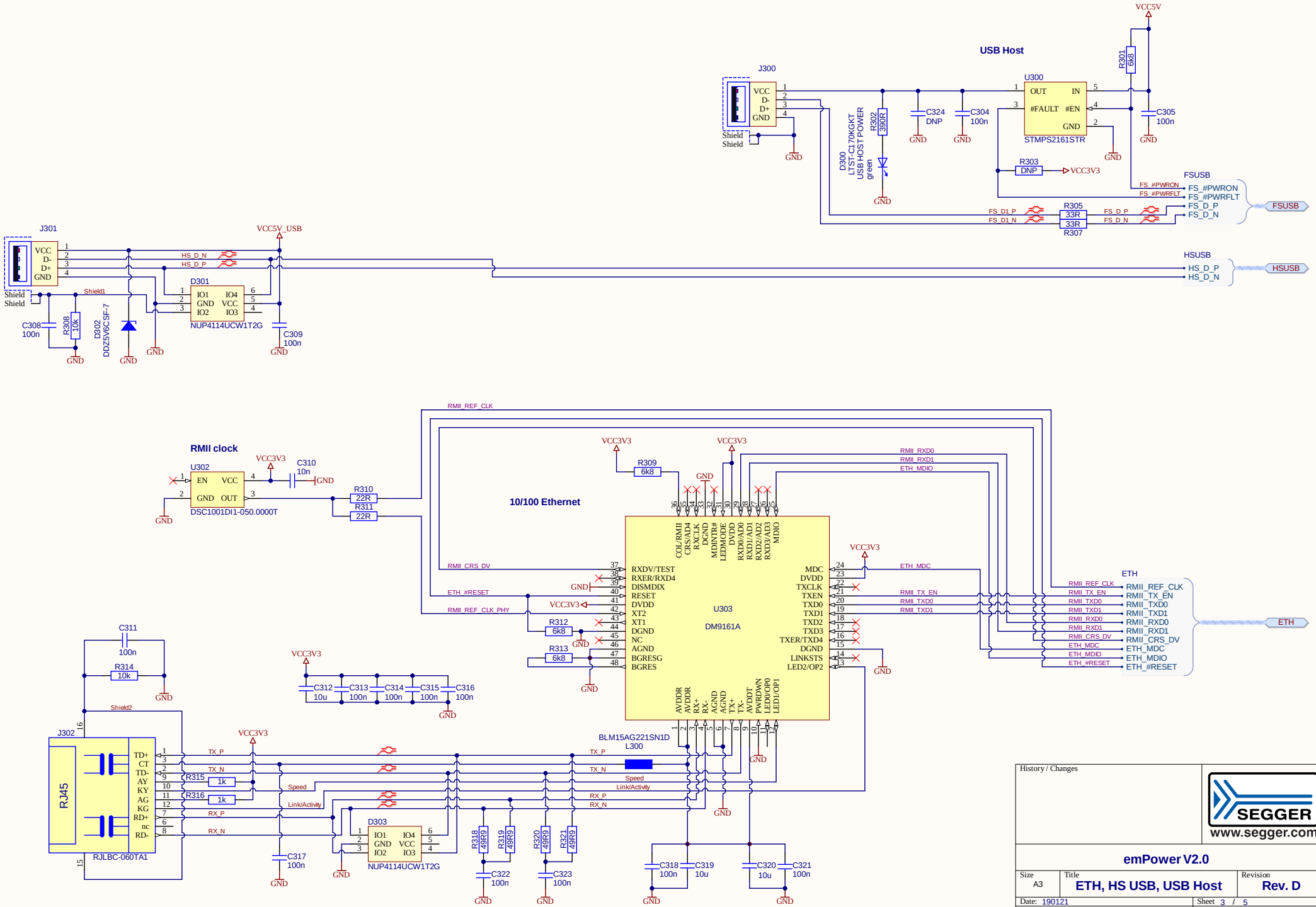
Date: 190121 Sheet 1 / 5

File: Sheet01_TopLevel.SchDoc Drawn: VK

***NOTES:**

- MCU GPIO pull up configuration: #SW_A ... #SW_E, #PB, SD_#CD, FS_#PWRFLT
- MCU does not provide a NAND Flash controller, GPIOs are used instead
- PTB18 (D_BLON), PTE7-9 (DIOx) are PWM capable timer outputs





History / Changes

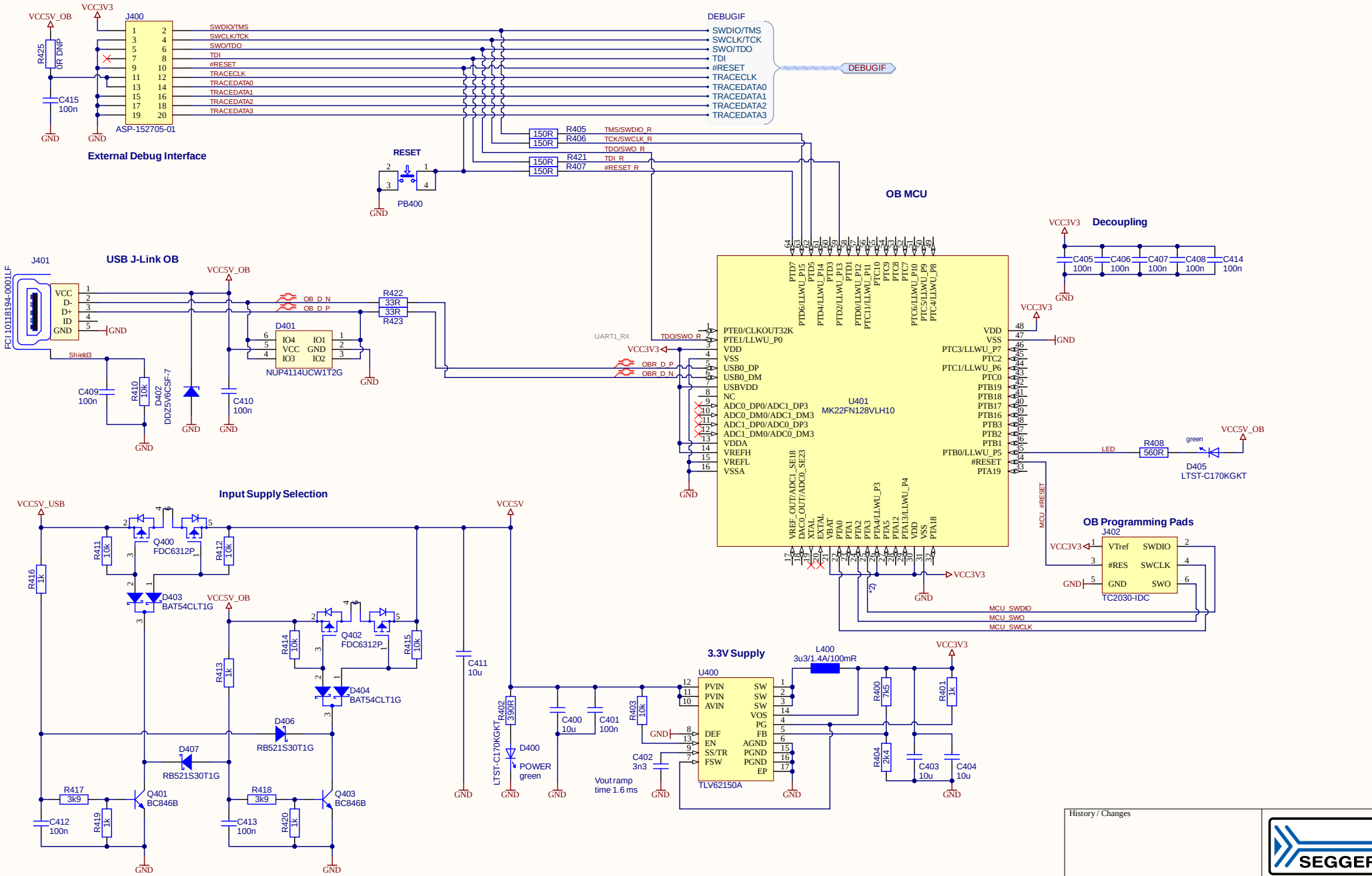
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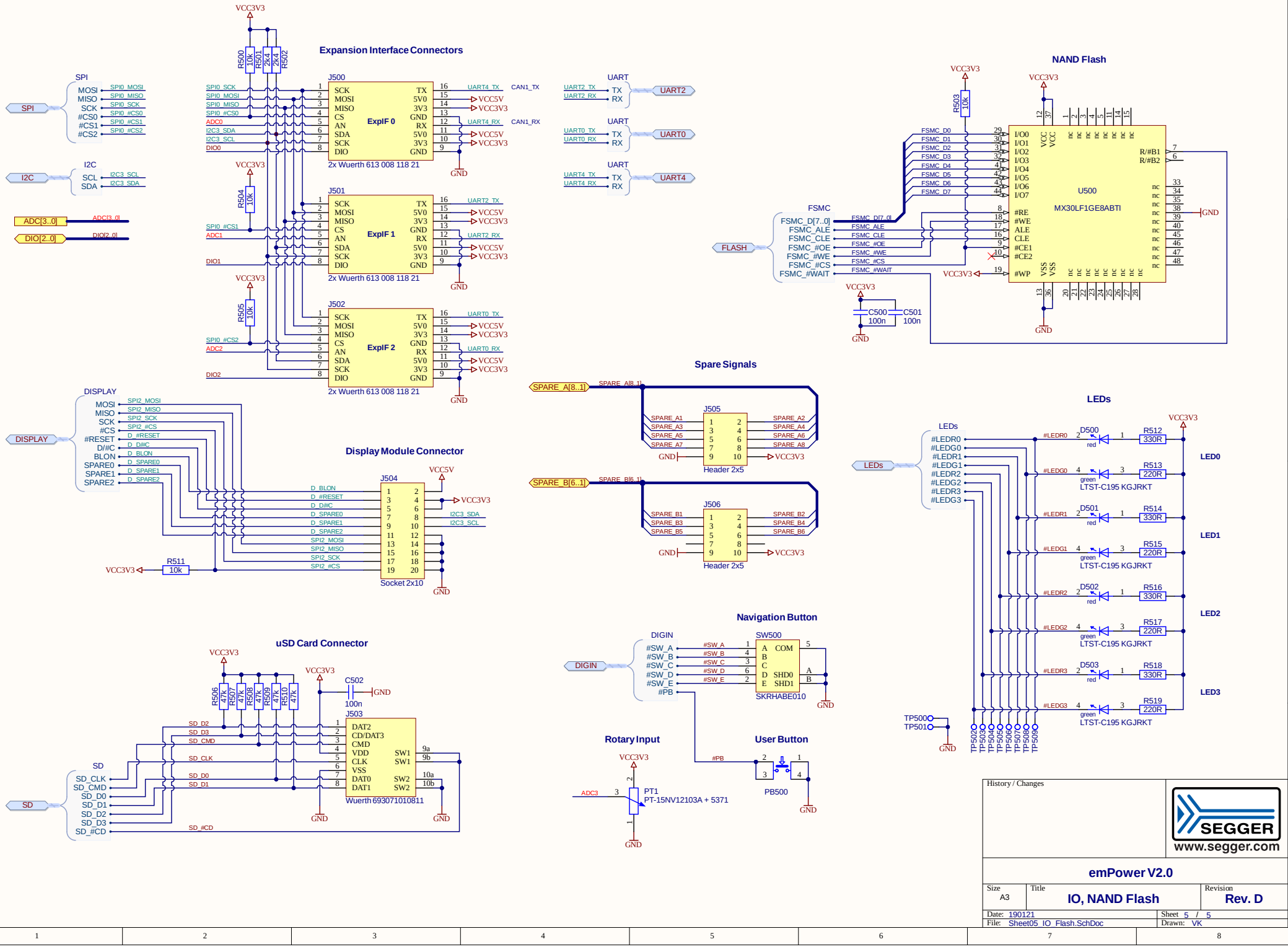
Size	Title	Revision
A3	ETH, HS USB, USB Host	Rev. D

Date: 190121 | Sheet 3 / 5

File: Sheet03_Ethernet_USB.SchDoc | Drawn: VK



History / Changes	SEGGER www.segger.com
emPower V2.0	
Size: A3	Title: J-Link OB, Power Supply
Revision: Rev. D	
Date: 190121	Sheet 4 / 5
File: Sheet04_OB_PwrSupply.SchDoc	Drawn: VK



History / Changes

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Size	Title	Revision
A3	IO, NAND Flash	Rev. D

Date: 190121 | Sheet 5 / 5

File: Sheet05_IO_Flash.SchDoc | Drawn: VK