SEGGER and Cadence team up to add native J-Link support for Cadence Tensilica cores

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SEGGER, a leading supplier of [software libraries](), [development tools](), [debug probes]() and [flash programmers](), today announced native [J-Link debug probe]() support for select instances of the Cadence Tensilica Processor IP, a portfolio of configurable and extensible controllers and DSPs.

The Cadence Tensilica cores supported in the first implementation phase are the Tensilica Xtensa LX7 CPU, a number of Tensilica HiFi DSPs (HiFi 4, HiFi 3z, HiFi 3, and HiFi 1), and also the Tensilica Fusion F1 DSP. The latest hardware versions of all commercial SEGGER J-Link models ([J-Link BASE](), [J-Link PLUS](), [J-Link ULTRA+](), and [J-Link PRO]()) now support high-speed download and debugging of these cores via JTAG and SWD.

"The SEGGER J-Link is the most widely used line of debug probes in the market," says Ivo Geilenbruegge, Managing Director of SEGGER. "J-Links have provided solid value to embedded development for over 15 years. Unparalleled performance, an extensive feature set, a multitude of supported CPUs, and compatibility with popular development environments — all this makes J-Link an unbeatable choice. We're happy to add Cadence Tensilica IP to the list of supported cores."

"The drive to push intelligence further out to the edge means that more and more MCUs and SoCs contain our Tensilica CPU and DSP IP," said George Wall, Group Director of Product Marketing for Tensilica Xtensa Processor IP at Cadence. "The new SEGGER implementation enables us to use the [J-Link GDB Server]() as a native J-Link driver in our Tensilica Xplorer Integrated Development Environment (IDE), resulting in a significant performance increase. As a result, customers will be able to debug their firmware running on Tensilica cores more quickly."

The Cadence Tensilica core support has already been added to the [J-Link software pack](), which is available for download from the SEGGER website.

For more information on J-Link, please visit:
https://www.segger.com/products/debug-probes/j-link/

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About SEGGER

SEGGER Microcontroller GmbH has three decades of experience in Embedded Systems, producing cutting-edge RTOS and Software Libraries, J-Link and J-Trace debug and trace probes, a line of Flasher In-System Programmers and software development tools.

SEGGER's all-in-one solution emPower OS provides an RTOS plus a complete spectrum of software libraries including communication, security, data compression and storage, user interface software and more. Using emPower OS gives developers a head start, benefiting from decades of experience in the industry.

SEGGER's professional embedded development software and tools are simple in design, optimized for embedded systems, and support the entire embedded system development process through affordable, high-quality, flexible and easy-to-use tools.

The company was founded by Rolf Segger in 1992, is privately held, and is growing steadily. SEGGER also has a U.S. office in the Boston area and branch operations in Silicon Valley, Shanghai and the UK, plus distributors on most continents, making SEGGER's full product range available worldwide.

For more information on SEGGER, please visit www.segger.com.

Why SEGGER?

In short, SEGGER has a full set of tools for embedded systems, offers support through the entire development process, and has decades of experience as the Embedded Experts.

In addition, SEGGER software is not covered by an open-source or required-attribution license and can be integrated in any commercial or proprietary product, without the obligation to disclose the combined source.

Finally, SEGGER offers stability in an often volatile industry, making SEGGER a very reliable partner for long-term relationships.

For additional information please visit: www.segger.com

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