

## SEGGER adds multicore support to SystemView

Monheim am Rhein, Germany—February 6th, 2025

**SEGGER's real-time software verification and visualization tool [SystemView](#) has been enhanced with multicore support, expanding its capabilities to systems with multiple CPU cores on a single chip.**

Using [SEGGER Real-Time Transfer technology](#), a single [J-Link debug probe](#) can now collect data from multiple cores in real time. Data is streamed to the host and is analyzed and visualized instantaneously. All events are recorded and can be saved for analysis and documentation purposes.

For every core that runs instrumented firmware, recordable events include task context switches, interrupt executions, function calls, heap and stack usage, data samples, log messages, and more.

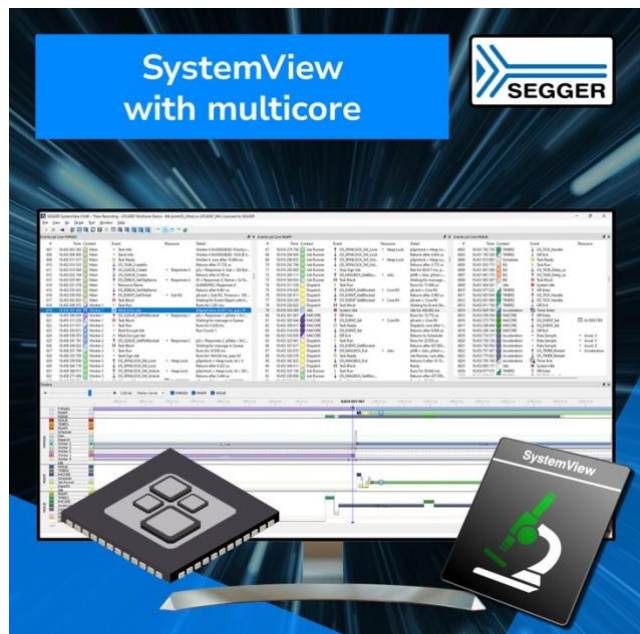
Each core records events to its own SystemView channel, just as it would do in a single-core recording scenario. Each CPU core can be viewed individually or as a combined group of all cores, making CPU interaction and related timing easy to see and verify.

SystemView is multi-platform (Linux, macOS, and Windows) and can be easily downloaded for use on Arm, Intel, or Apple silicon. Under SEGGER's Friendly License, no registration is required to download SystemView. Use is free of charge for educational and non-commercial purposes, and the software can be evaluated with no limits on code size, features, or time.

Included with SystemView are sample recordings, which aid users in obtaining a quick overview of how the tool works. No hardware is needed to get started, and it takes just five minutes to download, install, and begin evaluation.

"SystemView is unique in the industry, and multicore support makes it even more so," says Johannes Lask, Product Manager for SystemView at SEGGER. "We ask everyone who sees the potential here to simply give the tool a try. No product should hit the market without SystemView verification!"

SystemView's flexible instrumentation enables recording for a variety of real-time operating systems (RTOSs), as well as for applications with no operating system; and its multicore support extends this flexibility to each core in a respective system. Every core can run a different application and RTOS.



With SystemView's multicore support, no inter-core synchronization is required for multicore recording. Cores are not required to all run at the same speed; instead, each core can run on its own clock. Timestamps are generated with a single CPU cycle, down to one nanosecond of resolution. The timestamps are also correlated, and a unified system time is shown across all cores, so that users can see exactly what is happening on each core, in lockstep, as time passes.

### About SystemView

SystemView is a real-time recording and visualization tool for embedded systems. It reveals the true runtime behavior of an application, going far deeper than the system insights provided by debuggers. It is particularly effective when developing and working with complex embedded systems, and it can ensure a system performs as designed, track down inefficiencies, and find unintended interactions and resource conflicts.

SystemView's optimized target instrumentation enables data recording with cycle-accurate timestamps. All SystemView events are recorded, analyzed, and visualized while the target system is running, and they can be saved for documentation and analysis.

For more information, please visit the [SystemView](#) page at [www.segger.com](http://www.segger.com).

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### About SEGGER

SEGGER Microcontroller GmbH, founded in 1992, has over three decades of experience in embedded systems, producing cutting-edge [RTOS and software libraries](#), J-Link and J-Trace [debug and trace probes](#), a line of [Flasher in-system programmers](#), and [software development tools](#).

SEGGER's all-in-one solution [emPower OS](#) provides an RTOS plus a complete spectrum of software libraries for, among other things, communication, security, data compression and storage, user-interface software, and more. emPower OS gives developers a head start, allowing them to benefit from decades of experience in the industry.

SEGGER's professional embedded-development software and tools are simple in design, optimized for embedded systems, and support the entire embedded-system development process with their affordability, high-quality, flexibility, and ease of use.

SEGGER, with headquarters in Monheim am Rhein, Germany, also has an office in Boston, Massachusetts, United States, and branch operations in Silicon Valley, California, United States; Shanghai, China; and the United Kingdom. With distributors on most continents, SEGGER's full product range is available worldwide.

For more information on SEGGER, please visit [www.segger.com](http://www.segger.com).



## Why SEGGER?

In short, SEGGER has a full set of tools for embedded systems, offers support throughout the entire development process, and has decades of experience. We are The Embedded Experts.

Furthermore, SEGGER software is not covered by an open-source or an attribution-required license, and it can be integrated into any commercial or proprietary product—with no obligation to disclose the combined source. SEGGER offers stability in an often volatile industry, making it a highly reliable partner for long-term relationships.

For additional information, please visit [www.segger.com](http://www.segger.com).

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