

J-Link Unlimited Flash Breakpoints Now Free for Evaluation

Hilden, Germany - March 20th, 2012 – Today, SEGGER Microcontroller announces that unlimited Flash Breakpoints for J-Link are now freely available for evaluation and non-commercial use. This allows setting an unlimited number of breakpoints even if the application program is located in Flash memory, thereby utilizing the debugging environment to its fullest.

This feature has been optimized over years and is used by thousands of developers. Its clear advantages can now be experienced when using any J-Link, J-Link Ultra or J-Trace model without the need to request a license key by simply downloading the latest version of the J-Link related Software and Documentation Pack from <http://www.segger.com/jlink-software.html>.

If used in a commercial project, a license needs to be purchased when the evaluation is complete. There is no time limit on the evaluation period.

Behind the Technology

SEGGER Flash Breakpoints have been optimized to such a high level, the user will not even realize he has run out of hardware breakpoints during his debug session. J-Link reprograms the flash memory as little as possible. A number of optimizations are used, such as usage of hardware breakpoints whenever possible, automatic conversion of breakpoint types where applicable, instruction set simulation and emulation, flash cache, and other methods SEGGER has developed over the many years of this features evolution.

An unlimited number of breakpoints can be set anywhere in program memory; including the target device's, internal flash memory, external CFI-compliant flash, and internal or external RAM. Debugging limitations imposed by the number of hardware breakpoints available (2 on ARM7/9, 4 on Cortex-M0, M1 and typically 6 on Cortex-M3, M4) are completely eliminated.

This presents a flash debugging environment which is virtually the same as that when debugging in RAM.

“Unlimited Flash Breakpoints significantly improves the debugging experience for anybody developing code located in Flash memory. I personally use it all the time and see it as a must-have for every professional developer” says Rolf Segger, founder and CTO of SEGGER.

About J-Link

The SEGGER J-Link is the industry-standard for ARM debug emulators, supported by all major tool chains for ARM cores. The SEGGER J-Link is independent and will work with IDEs from: Freescale, IAR, KEIL, Mentor Graphics, Rowley, Renesas, Tasking, Phyton and others. In addition to those listed above; any RDI compliant debugger can be used with the optional RDI module, and any GDB compliant debugger with the free GDB-Server. Therefore; as projects change a different compiler/debugger might become necessary. With the J-Link family investments (monetary and learning curve) in development/production tools are preserved. Setup of a J-Link is done in mere minutes.

J-Link supports multiple CPU families, such as ARM 7/9/11, Cortex-M0,M1,M3,M4,R4,A5,A8,A9, Renesas RX in a single model; there is typically no need to buy a new J-Link or new license when switching to a different CPU family. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free





updates are included with even the baseline model of the J-Link. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with the IAR IDE, or any other development environment you choose.

Full product specifications are available at: <http://www.segger.com/jlink.html>

The J-Link-Software is available at: http://www.segger.com/download_jlink.html

U.S. On-Line Web Shop: <http://shop-us.segger.com>

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER's intention is to cut software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at <http://www.segger.com>.

Contact information:

Dirk Akemann,
Marketing Manager
Tel: +49-2103-2878-0
E-mail: info@segger.com

Issued on behalf of:

SEGGER Microcontroller GmbH & Co. KG
In den Weiden 11
40721 Hilden
Germany
www.segger.com

SEGGER Microcontroller Systems LLC
106 Front Street
Winchendon, MA 01475
United States of America
www.segger-us.com

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