SEGGER announces a beta release of their new J-Link Debugger

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SEGGER invites the J-Link/J-Trace community to join in testing a free Beta release of the new J-Link Debugger software, a full-featured graphical debugger for embedded applications.

With J-Link Debugger it is possible to debug any embedded application on the C source and assembly language level. It can load applications built with any toolchain/IDE or debug the target's resident application without any source.

J-Link Debugger increases development speed by extending the rich feature set and robust high performance of the J-Link/J-Trace family of debug probes. Scriptable project files automate setup making it easy to get started.

Ultra-fast flash downloads, an unlimited number of breakpoints even when debugging in flash memory and the SEGGER Real-Time-Terminal are all features which may be utilized from within the SEGGER J-Link Debugger. The SEGGER Real-Time-Terminal feature alone will drastically change the way in which debugging is done.

Used with one of the high-end trace models (J-Trace Cortex-M) the J-Link Debugger supports instruction trace.

"SEGGER J-Link Debugger is the latest addition to the extensive family of J-Link software tools which include J-Flash, J-Link Commander, J-Link GDB Server, J-Link RDI/RDDI, J-Scope, J-Link SWO Viewer, and J-Mem," says Alexander Gruener, SEGGER J-Link Product Manager.

More information about J-Link Debugger can be found here: http://www.segger.com/j-link-debugger.html

More information on J-Link is available at: http://segger.com/jlink.html

About J-Trace

J-Trace is the most feature rich offering of the J-Link family adding instruction trace capabilities using ARM's Embedded Trace Macrocell (ETM). The J-Trace models have an internal trace memory. Their direct interface to the ETM allows maximum trace frequency. ETM instruction trace allows the developer to look at the history of a program's execution. This is useful, for example, when a program crash is caused by an unexpected jump. In this case the developer can track back to where the program execution left its intended flow. Full product specifications are available at: http://www.segger.com/j-trace-for-cortex-m.html

About J-Link

The SEGGER J-Link is the most popular family of debug probes on the market. It is tool chain independent and works with free GDB-based tool chains such as emIDE and Eclipse, as well as commercial IDEs from: Atmel, Atollic, Coocox, Cosmic, Freescale, IAR, KEIL, Mentor Graphics, Microchip, Python, Rowley, Renesas, Tasking and others. With the J-Link family, investments in the debug probe are preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M, Cortex-R, Cortex-A as well as Renesas RX100, RX200, RX600 and Microchip PIC32; there is no need to buy a new J-Link or new license when switching to a different yet supported CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most
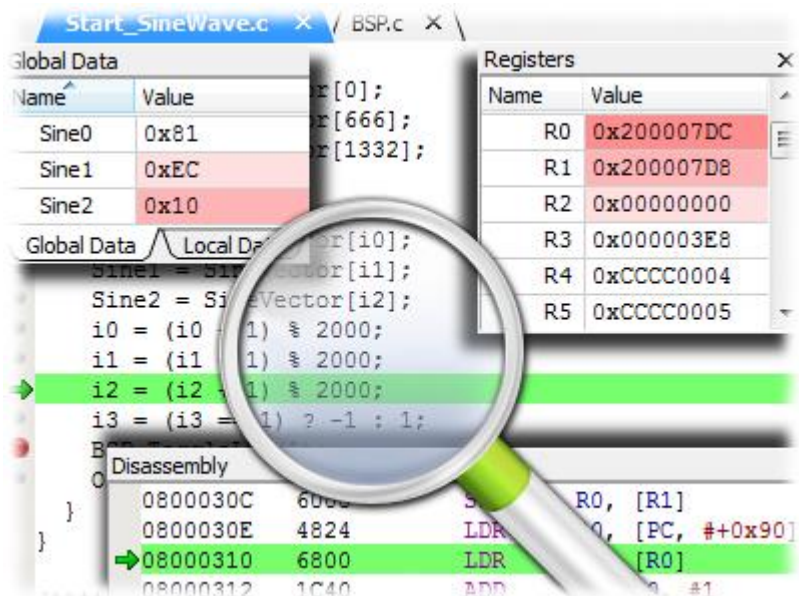
cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link family. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Full product specifications are available at: http://segger.com/jlink.html

The J-Link-Software is available at: http://segger.com/download_jlink.html

U.S. On-Line Web Shop: http://shop-us.segger.com

Online Shop (Europe, Asia, Africa): http://shop.segger.com

About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER cuts software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at http://www.segger.com.

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